

11/21/2006

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

**Title of
Invention**

INTERFACE RECEIVE CIRCUITS FOR MODULARIZED DATA
OPTIMIZATION ENGINES AND METHODS THEREFOR

Application Number : 10/026368
Confirmation Number: 8054
First Named Applicant: Isaac Achler
Attorney Docket Number: ANTAP005/IC-0106
Art Unit: 2862
Examiner:
Search string: (5805600 or 5530645 or 6038226).pn



US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
LY	1	5805600	1998-09-08	Venters et al.	—	—	—
LY	2	5530645	1996-06-25	Chu	—	—	—
LY	3	6038226	2000-03-14	Ellersick et al.	—	—	—

Signature

Examiner Name	Date
	5/27/2005

12/02/2001
In re application of: ACHLER
Serial No.: 10/026,368
Filed: December 17, 2001

Title: INTERFACE RECEIVE CIRCUITS
FOR MODULARIZED DATA
OPTIMIZATION ENGINES AND
METHODS THEREFOR

Group Art Unit: 2665
Examiner: VU, HUY DUY
Docket: ANTAP005/IC-0106
Confirmation No.: 8054

INFORMATION DISCLOSURE STATEMENT

US PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant	Reference to Related Case

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date	Applicant	Reference to Related Case	T

OTHER DOCUMENTS

Examiner Initials	Cite No.		T
LY		Robert W. Kembel; "Fibre Channel: A Comprehensive Introduction", Northwest Learning Associates, Inc., Tucson, AZ 2000	
LY		Mark Nelson; "LZW Data Compression", http://www.dogma.net/markn/articles/lzw/lzw.htm . Originally published in October 1989 Dr. Dobb's Journal (2800 Campus Drive, San Mateo, CA. www.ddj.com)	
LY		Terry A. Welch; "A Technique for High Performance Data Compression", IEEE Computer 17(6), June 1984, pp. 8-19	

Examiner Signature		Date Considered	5/27/2005
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: ACHLER Serial No.: 10/026,368 Filed: December 17, 2001 Title: INTERFACE RECEIVE CIRCUITS FOR MODULARIZED DATA OPTIMIZATION ENGINES AND METHODS THEREFOR	Group Art Unit: 2665 Examiner: VU, HUY DUY Docket: ANTAP005/IC-0106 Confirmation No.: 8054
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INFORMATION DISCLOSURE STATEMENT**US PATENT DOCUMENTS**

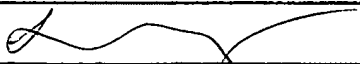
Examiner Initials	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant	Reference to Related Case

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date	Applicant	Reference to Related Case	T

OTHER DOCUMENTS

Examiner Initials	Cite No.	Description	T
LY		US Patent Application No.: 10/026,370 Filed: December 17, 2001 Title: "Architectures for a modularized data optimization engine and methods therefore" Docket: ANTAP001/IC-0102	
LY		US Patent Application No.: 10/026,680 Filed: December 17, 2001 Title: "Integrated circuits for a high speed adaptive compression and methods therefore" Docket: ANTAP002/IC-0103	
LY		US Patent Application No.: 10/026,679 Filed: December 17, 2001 Title: "Interface circuits for modularized data optimization engines and methods therefore" Docket: ANTAP004/IC-0105	

Examiner Signature	Date Considered
	5/27/2005